

Schematic Styleguide

To get a more readable Schematic for all people it would be good to follow this styleguide.

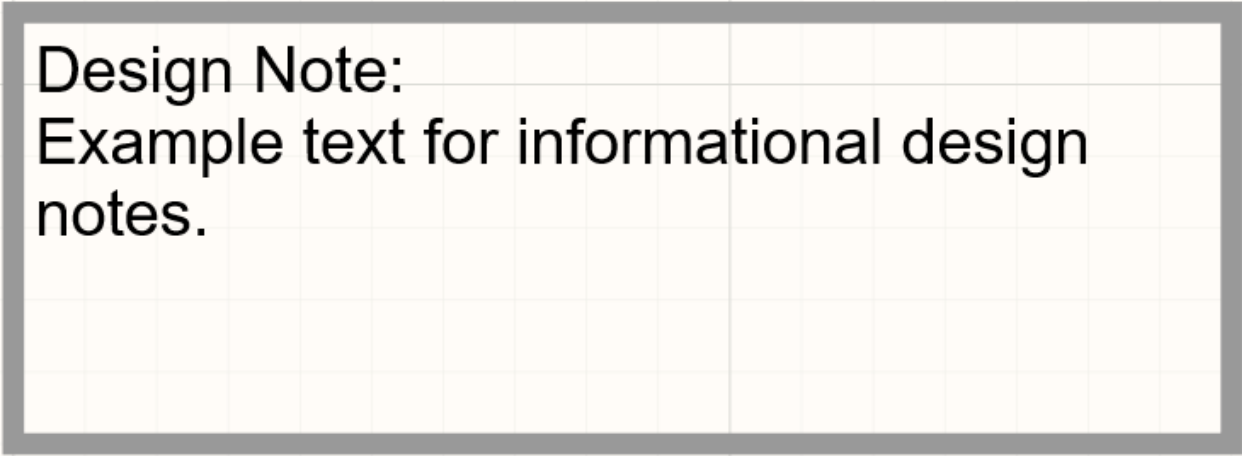
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Design Notes

Here are the relevant colors and Styles how to display them: (all Borders are with line width medium)

Info

For informational design notes use:



Design Note:
Example text for informational design notes.

Border: Color(Hex #999999) Text: Color (Black) Font ARIAL

Caution

For cautionary design notes use:



Design Note:
Example text for cautionary design notes.

Border: Color(Hex #FFFF00) Text: Color (Black) Font ARIAL

Debug

For debug design notes use:

Design Note:
Example text for debug design notes.

Border: Color(Hex #FF9900) Text: Color (Black) Font ARIAL

Critical

For critical design notes use:

Design Note:
Example text for critical design notes.

Border: Color(Hex #FF0000) Text: Color (Hex #FF0000) Font ARIAL Bold

Layout

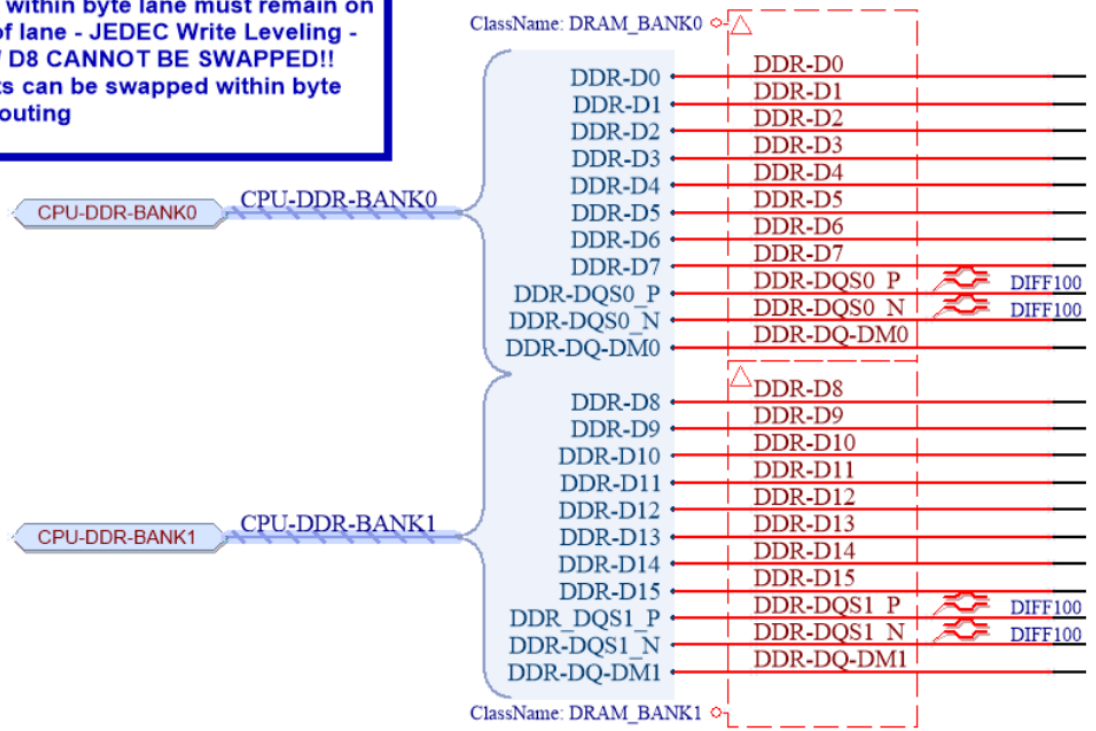
For layout guideline notes use:

Layout Note:
Example text for layout guidelines.

Border: Color(Hex #0000FF) Text: Color (Hex #0000FF) Font ARIAL

Here is one example for a layout guideline:

LAYOUT NOTE:
 -Lowest order bit within byte lane must remain on lowest order bit of lane - JEDEC Write Leveling - JESD79-3E - D0 / D8 CANNOT BE SWAPPED!!
 -All other data bits can be swapped within byte lane to improve routing



TOP Sheet

Every Design should have a Top Sheet. It would be nice to add a Sheet Legend. For that a hierarchical or strict hierarchical approach for the schematic design must be used.

One Example:

Project Name - TOP LEVEL

